harmonic-limited extraction of a D.C. voltage from an A.C. power system.

6. (Amended) The supply circuit as claimed in claim 2, in which the operating function of the oscillator circuit (OS) is carried out as an oscillator circuit on the one hand, and as an amplifier circuit on the other, as a function of the circuit state of the input (E) of the oscillator circuit.

Please add the following claims:

--11. The supply circuit as claimed in claim 2, which is a power factor correction circuit (L1, L2, C1, D1, T1) for the harmonic-limited extraction of a D.C. voltage from an A.C. power system.--

--12. The supply circuit as claimed in claim 3, in which the operating function of the oscillator circuit (OS) is carried out as an oscillator circuit on the one hand, and as an amplifier circuit on the other, as a function of the circuit state of the input (E) of the oscillator circuit.--

--13. The supply circuit as claimed in claim 4, in which the operating function of the oscillator circuit (OS) is carried out as an oscillator circuit on the one hand, and as an amplifier circuit on the other, as a function of the circuit state of the input (E) of the oscillator circuit.--

--14. The supply circuit as claimed in claim 5, in which the operating function of the oscillator circuit (OS) is carried out as an oscillator circuit on the one hand, and as an amplifier circuit on the other, as a function of the circuit state of the input (E) of the oscillator circuit.--

--15. The supply circuit as claimed in claim 12, in which the oscillator circuit has a digital input which is connected to the output of the forcing circuit, and operates as a driver circuit when there is an input level of logic 0 or logic 1, and operates as an oscillator circuit when there is an input level in a nonspecific intermediate region.--

--16. The supply circuit as claimed in claim 13, in which the oscillator circuit has a digital input which is connected to the output of the forcing circuit, and operates as a driver circuit when there is an input level of logic 0 or logic 1, and operates as an oscillator circuit when there is an input level in a nonspecific intermediate region.--

--17. The supply circuit as claimed in claim 14, in which the oscillator circuit has a digital input which is connected to the output of the forcing circuit, and operates as a driver circuit when there is an input level of logic 0 or logic 1, and operates

as an oscillator circuit when there is an input level in a nonspecific intermediate region.--

--18. The supply circuit as claimed in claim 12, in which the oscillator circuit (OS) operates as an amplifier circuit when there is a low impedance of the input (E) with respect to a reference potential, and as an oscillator circuit when there is a high impedance of the input (E) with respect to the reference potential.--

--19. The supply circuit as claimed in claim 13, in which the oscillator circuit (OS) operates as an amplifier circuit when there is a low impedance of the input (E) with respect to a reference potential, and as an oscillator circuit when there is a high impedance of the input (E) with respect to the reference potential.--

--20. The supply circuit as claimed in claim 14, in which the oscillator circuit (OS) operates as an amplifier circuit when there is a low impedance of the input (E) with respect to a reference potential, and as an oscillator circuit when there is a high impedance of the input (E) with respect to the reference potential.--